

## Refine Search

### Search Results -

Terms	Documents
L4 near4 L7	0

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L8





### Search History

DATE: Thursday, March 15, 2007   
 [Purge Queries](#)   
 [Printable Copy](#)   
 [Create Case](#)

**Set Name**   **Query**  
 side by side

**Hit Count**   **Set Name**  
 result set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L8</u>	l4 near4 L7	0	<u>L8</u>
<u>L7</u>	l3 near3 L6	300	<u>L7</u>
<u>L6</u>	pld or pla or (programmable adj2 logic)	36012	<u>L6</u>
<u>L5</u>	l4 near3 l3	26	<u>L5</u>
<u>L4</u>	prefetch\$	11700	<u>L4</u>
<u>L3</u>	l1 near2 l2	94932	<u>L3</u>
<u>L2</u>	(data or info or information)	5358866	<u>L2</u>
<u>L1</u>	(config or configuration)	2088185	<u>L1</u>

END OF SEARCH HISTORY


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Sitemap](#) | [Help](#)

Welcome United States Patent and Trademark Office

☐ Search Results[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "((((((prefetch\*)&lt;in&gt;metadata))&lt;and&gt;((config &lt;or&gt; configuration) &lt;and&gt; (data ..."

Your search matched **1** of **149** documents.☒ e-mail printer friendlyA maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

## » Search Options

[View Session History](#)[New Search](#)

## Modify Search

((((((prefetch\*)<in>metadata))<and>((config <or> configuration) <and> (data <or> info)  ☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **A 390-mm<sup>2</sup>, 16-bank, 1-Gb DDR SDRAM with hybrid bitline architecture**  
 Kirihaata, T.; Mueller, G.; Ji, B.; Frankowsky, G.; Ross, J.M.; Terletzki, H.; Netis, D.G.;  
 Weinfurtner, O.; Hanson, D.R.; Daniel, G.; Hsu, L.L.-C.; Sotraska, D.W.; Reith, A.M.; Hug, M.A.;  
 Guay, K.P.; Selz, M.; Poechmueller, P.; Hoenigschmid, H.; Wordeman, M.R.;  
Solid-State Circuits, IEEE Journal of  
 Volume 34, Issue 11, Nov. 1999 Page(s):1580 - 1588  
 Digital Object Identifier 10.1109/4.799866  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(628 KB\)](#) IEEE JNL  
[Rights and Permissions](#)

indexed by  
 Inspec<sup>®</sup>[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2006 IEEE – All Rights Reserved



Welcome United States Patent and Trademark Office

[Search Session History](#)[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Edit an existing query or  
compose a new query in the  
Search Query Display.

Thu, 15 Mar 2007, 4:45:52 PM EST

## Search Query Display

Select a search number (#)  
to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

## Recent Search Queries

		Results
<u>#1</u>	((prefetch*)<in>metadata)	809
<u>#2</u>	(((((prefetch*)<in>metadata))<AND>((config <or> configuration) <and> (data <or> information)<in>metadata)))	149
<u>#3</u>	((((((((prefetch*)<in>metadata))<and>((config <or> configuration) <and> (data <or> information)<in>metadata)))<AND>(pld <or> pla <or> (programmable <and> logic)<in>metadata)))	1

